2019 IEEE Technical Meeting on Reliable, Safe, Secure, and Time-Deterministic Intelligent Systems

6 DECEMBER 2019
GRAND HOTEL MAJESTIC GIÀ BAGLIONI, VIA DELL'INDIPENDENZA 8, BOLOGNA, ITALY
09:30-10:00 Opening Session & Welcome Messages
Cecilia Metra, 2019 IEEE Computer Society President, and IEEE Digital Reality Initiative Project on Reliable, Safe, Secure and Time Deterministic Intelligent Systems Co-Chair
Riccardo Mariani, IEEE Computer Society Vice-President for Standards and IEEE Digital Reality Initiative Project on Reliable, Safe, Secure and Time Deterministic Intelligent Systems Co-Chair
Davide Moro, President of the School of Engineering of the University of Bologna
Vincenzo Piuri, Chair, IEEE Italy Entrepreneurship Committee

10:00 – 13:10 Keynote Speakers
10:00-10:25 Roberto Saracco, Chair IEEE Digital Reality Initiative
10:25-10:50 Nineta Polemi, Programme Manager - Policy Officer at European Commission - Cyber Security, Technologies and Capacity Building
10:50-11:15 Konstantinos Karachalios, IEEE Standards Association Managing Director
11:15-11:30 Coffee Break
11:30-11:55 Riccardo Mariani, Industry Safety Vice-President Nvidia
11:55-12:20 Yervant Zorian, Synopsys Fellow and Chief Architect, President of Synopsys Armenia
12:20-12:45 Ranieri Niccoli, Industrial Director Lamborghini

13:10-14:30 Lunch

14:30-16:15 Panel: Challenges for Reliable, Safe, Secure and Time Deterministic Intelligent Systems
Moderator: Cecilia Metra
Panelists:
IP View: Pete Harrod, Director of Functional Safety, Arm
EDA View: Meirav Nitzan, Program Management Director, Functional Safety Solutions, Verification, Synopsys
System View: Filippo Brenna, Sr. Solution Architect, NVIDIA Italy
Enrico Sampó, Senior Vehicle Dynamicist, Ferrari
Diego Marino, Engineering Director Italy, Bombardier
Bruno Meyer, Senior Executive, Réseau de Transport d'Électricité (RTE)
Lorenzo Chiarion, Head of Manufacturing Engineering, Lamborghini
Technology View: Jyotika Athavale, Principal Engineer, Senior Functional Safety and RAS Architect, Intel
SoC View: Arcangelo Bruna, Manager, STMicroelectronics
5G Communication View: Ritu Sethi, Software Architect, Intel
Security View: Sohrab Aftabjahani, Staff Security Researcher, Intel

16:15-16:30 Coffee Break

16:30–16:45 Hermann Brand, European Standards Affairs Director

16:45-18:30 Working Session
16:45-17:00 Introduction of Meeting Working Document: Riccardo Mariani
17:00-18:00 Working Session: Teams Will Be Formed on the Four Topics:
1. Reliability
2. Safety
3. Security
4. Time Determinism
   Each team will have 1 hour of time to review the meeting working document and propose changes
18:00-18:10 Michela Degli, Message from Deputy Rector of University of Bologna
18:10-18:30 Report of the Four Teams

18:30-18:40 – Riccardo Mariani, Summary and next
18:40-18:50 – Closing Messages: Riccardo Mariani and Cecilia Metra