



IEEE
COMPUTER
SOCIETY

W. Wallace McDowell Award

Call for Award Nominations

Deadline: 1 October 2020

2019 W. Wallace McDowell Award Recipient



Rajesh K. Gupta

University of California,
San Diego

*For seminal
contributions in design
and implementation
of Microelectronic
Systems-on-Chip and
Cyberphysical Systems.*

A certificate and \$2,000 honorarium are presented for outstanding recent theoretical, design, educational, practical, or other similar innovative contributions that fall within the scope of IEEE Computer Society interest.

About William Wallace McDowell

Mr. McDowell's research and engineering group was responsible for 99 diversified and highly important development projects for the U.S. government. He directed the post-war conversion to commercial efforts and the re-engineering of IBM's entire product line. He was responsible for the transition from electromechanical techniques to electronics, and the subsequent transition to solid state devices.

He later was responsible for development of major advances including IBM's card-programmed calculator, magnetic drums and tape units, magnetic core and disc storage, the company's "700" systems, and the Naval Ordinance Research Calculator.

Nomination Requirements

This award requires a minimum of three endorsements.

Nominations are being accepted electronically by
1 October 2020 to bit.ly/mcdowell-award.

Questions?

Visit bit.ly/mcdowell-award.

or contact awards@computer.org

